ABSTRACT OF THE DISCLOSURE

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A semiconductor device capable of improving the operating speed and inhibiting the threshold voltage from fluctuation is obtained. In this semiconductor device, fluorine is introduced into at least any of regions extending over the junction interfaces between a first conductivity type semiconductor region and second conductivity type source/drain regions, at least the interface between the gate insulator film and the central region of a channel region as well as a gate insulator film, and side wall insulator films.